



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814

Examiner: MAI, ANH D

In Re PATENT APPLICATION Of:

Applicant : Hiroki NAKAMURA)

Serial No. : 09/625,178)

Filed : July 25, 2000)

For : SEMICONDUCTOR DEVICE HAVING)
WIRING PATTERNS AND DUMMY)
PATTERNS COVERED WITH)
INSULATING LAYER)

AMENDMENT
AFTER
ALLOWANCE

Attorney Ref. : F98ED0762)

November 28, 2005

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37CFR §1.312, please amend the above-identified

application as follows:

*Please Enter
A.M. 12/12/2005*